Total No. of Questions: 12]	SEAT No. :
P3179	[Total No. of Pages :3

[4859]-188

B.E. (INFORMATION TECHNOLOGY)

		Embedded System (Semester - I)	
		(2008 Pattern)(Elective -II)	
Time: 3 Hours] [Maximum Mark		: 100	
Instr		ons to the candidates:	_
	<i>1) 2)</i>	Answers to the two sections should be written in separate answer book In section I attempt: Q.No. 1 or Q.No.2, Q.No. 3 or Q.No.4, Q.No. 5 or Q. In section II attempt: Q.No. 7 or Q.No.8, Q.No. 9 or Q.No.10, Q.No. Q.No.12,	<i>No.6</i> ,
	3)	Neat diagrams must be drawn wherever necessary.	
	<i>4)</i>	Figures to the right indicate full marks.	
	5)	Assume Suitable data, if necessary.	
		SECTION-I	
Q 1)	a)	What are the design parameters of embedded systems? Explain.	[8]
	b)	What do you mean by DSP, SoC and ASSP?	[8]
		OR	
Q2)	a)	What are the embedded systems? Classify them.	[8]
	b)	What are the different components of an embedded system?	[8]
Q 3)	a)	Explain different architectural features considered while selecture microprocessors or microcontrollers for an embedded system?	cting [8]
	b)	What are the techniques of power management used while designing embedded system?	ng an [4]
	c)	What are the types of memory selected and their typical size verified designing the data acquisition system?	while [6]
		OR	
Q4)	a)	How a designer selects processor, EPROM and RAM required digital camera?	for a [6]

- - What is UART? How is it useful in an embedded system? **[6]**

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Explain the typical memory map for a small scale embedded application. [6] c) Compare RS-233C and RS-425 communication protocols. **Q5)** a) [8] b) Describe SPI protocol in brief and the applications where it is preferred. [8] OR What is CAN protocol? Give its features and applications. [8] **Q6**) a) How does host recognize the device insertion in USB protocol? Explain b) in detail. [8] **SECTION-II** What are the different phases of software development cycle for a typical **Q7**) a) embedded system? [8] What are the different debugging tools available for embedded system b) programming [6] What are the advantages of using high level language instead of assembly c) language for embedded system programming? [4] OR Compare Java and C++ programming and their suitability for embedded **Q8**) a) systems. [6] What is cross compiler? Name one. How it is different than generic b) compiler? [6] With an example explain how stacks and queues are used to implement c) application functionality in embedded system software. [6] **Q9**) a) What are the different characteristics of real time operating system? Give two example of RTOS. [6] With the help of neat diagram, explain cooperative scheduling model for b) RTOS. What is interrupt latency time for this scheduling model. [10]OR With the help of neat diagram, explain preemptive scheduling for RTOS.[8] *Q10*)a)

- b) Define and explain interrupt latency period. What is its significance in RTOS? [4]
- c) What is a mailbox? Give details. [4]
- Q11)a) Differentiate MicroC/OS-II and VxWorks based on features and their area of application. [6]
 - b) With the help of neat system block diagram, explain the system requirements and tasks for adaptive cruise control system for a car.[10]

OR

- Q12)a) How tasks are managed in MicroC/SO-II? Explain in detail. [8]
 - b) With help of neat diagram, explain synchronization of tasks and IPCs for vending machine application. [8]

