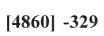
Total	l No	. of Questions : 8] SEAT No. :	
P4 1	126	[Total No. of Pages [4860] - 329	s : 2
M.I	E. (Computer Engineering) & (Computer Network) (Semester -	· I)
	E	MERGING TRENDS IN COMPUTER ARCHITECTURE	
		(2008 Pattern)	
Time	:3	Hours] [Max. Marks:	100
Instr	ucti	ons to the candidates:	
	1)	Solve any three questions from each section.	
	<i>2)</i>	Answer to the two sections should be written in separate answer books.	
•	3)	Figures to the right indicate full marks.	
		SECTION - I	
Q1)	a)	Explain the architecture of Itanium.	[8]
	b)	Differentiate between IA-32 architecture and IA-64 architecture.	[8]
Q2)	a)	What is multicare architecture? Explain with examples.	[8]
	b)	Explain symmetric and asymmetric multiprocessors with relat advantages and disadvantages.	ive [8]
Q3)	a)	Explain PARAM net in detail.	[8]
	b)	Explain Giga Bit Ethernet and Quadrinet in brief.	[8]
Q4)	Wı	rite short notes on any three:	18]
	a)	Cluster of Workstations (COWs) and Network of Workstations (NOW	Vs)

b) Blade serversc) Direct Attached Storage (DAS) and Network Attached Storage (NAS)

d) Storage Area Network (SAN)

SECTION - II

Q5)	a)	Explain the different types of multithreading.	[8]
	b)	Explain TCP/IP protocol suite.	[8]
Q6)	a)	Explain Sneha - Samuham computing model.	[8]
	b)	Explain log P communication model.	[8]
Q7)	a)	Explain shared memory programming and message passing programming	ng. [8]
	b)	Explain data parallel programming model.	[8]
Q8)	Writ	e short notes on any three:	8]
	a)	ATM switches and networks	
	b)	ARC model	
	c)	ARCC model	
	d)	High performance FORTRAN	



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