

Total No. of Questions : 8]

SEAT No. :

P4126

[4860] - 329

[Total No. of Pages : 2

M.E. (Computer Engineering) & (Computer Network) (Semester - I)
EMERGING TRENDS IN COMPUTER ARCHITECTURE
(2008 Pattern)

Time : 3 Hours]

[Max. Marks : 100

Instructions to the candidates :

- 1) Solve any three questions from each section.*
- 2) Answer to the two sections should be written in separate answer books.*
- 3) Figures to the right indicate full marks.*

SECTION - I

- Q1)** a) Explain the architecture of Itanium. [8]
b) Differentiate between IA-32 architecture and IA-64 architecture. [8]
- Q2)** a) What is multicore architecture? Explain with examples. [8]
b) Explain symmetric and asymmetric multiprocessors with relative advantages and disadvantages. [8]
- Q3)** a) Explain PARAM net in detail. [8]
b) Explain Giga Bit Ethernet and Quadernet in brief. [8]
- Q4)** Write short notes on any three : [18]
a) Cluster of Workstations (COWs) and Network of Workstations (NOWs)
b) Blade servers
c) Direct Attached Storage (DAS) and Network Attached Storage (NAS)
d) Storage Area Network (SAN)

P.T.O.

SECTION - II

Q5) a) Explain the different types of multithreading. [8]

b) Explain TCP/IP protocol suite. [8]

Q6) a) Explain Sneha - Samuham computing model. [8]

b) Explain log P communication model. [8]

Q7) a) Explain shared memory programming and message passing programming. [8]

b) Explain data parallel programming model. [8]

Q8) Write short notes on any three : [18]

a) ATM switches and networks

b) ARC model

c) ARCC model

d) High performance FORTRAN

