OCTOBER 2018 / IN - SEM (T2)

F. Y. M. TECH. (E&TC) (SEMESTER -I)

COURSE NAME: Advanced Embedded Processors and Programming

COURSE CODE: ETPA11182

(PATTERN 2018)

Scheme of Marking

Q.1)

a) Justification of Cortex architecture popularity in modern embedded applications with at least 05 reasons (01 mark each) with proper justification (01 mark each)

OR

Q.2)

a) Any six comparison points of Compare Cortex M3 architecture with classic series of ARM. (01x 06) Marks

b) Factors for interrupt response -01 marks

Cortex method of NVIC tail chaining – 03 marks