

P118-142 (T2)

Total No. of Questions – [2]

Total No. of Printed Pages: [1]

G.R. No.	
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OCTOBER 2018 / IN - SEM (T2)

F. Y. M. TECH. (E&TC) (SEMESTER -I)

COURSE NAME: Advanced Embedded Processors and Programming

COURSE CODE: ETPA11182

(PATTERN 2018)

Time: [30 Minutes]

[Max. Marks: 10]

(*) Instructions to candidates:

- 1) Figures to the right indicate full marks.
- 2) Use of scientific calculator is allowed
- 3) Use suitable data where ever required

Q.1)

- a) Why Cortex architecture is popular for modern embedded applications?
Justify with reasons.

10 Marks

OR

Q.2)

- a) Compare Cortex M3 architecture with classic series of ARM.
- b) What makes cortex architecture to control interrupt response time effectively?

06 Marks

04 Marks