

OCTOBER 2018/IN-SEM (T2)

S. Y. B. TECH. (PROGRAM) (SEMESTER - I)

COURSE NAME: Digital Electronics

COURSE CODE: ETUA21175

(PATTERN 2017)

Marking Scheme

Q.NO	Sub Q.N O	Marking Scheme	Marks	Difficulty Level	Cognitive level	CO Mapped
Q1	a)	Output waveform: Positive edge triggered [2] Negative edge triggered [2] explanation [2]	[6]	H	Applying	CO3
	b)	Logic Diagram [2] Waveform [2] Explanation [2]	[6]	M	Applying	CO3
	c)	Output waveform [4]	[4]	L	Analysing	CO3
OR						
Q2	a)	Output waveform	[6]	H	Applying	CO4
	b)	Drawbacks [2] Explanation [2] Logic Diagram [2]	[6]	M	Applying	CO4
	c)	Logic Diagram [1] Waveform [1] Explanation [2]	[4]	L	Analysing	CO3
OR						
Q3	a)	Truth table [2] K-map [2] Logic diagram [2]	[6]	M	Analysing	CO4
	b)	Logic diagram [1] Truth table [1] waveform [2]	[4]	L	Applying	CO3
	c)	Truth table [2] Logic diagram [2]	[4]	L	Applying	CO3
OR						
Q4	a)	Output waveform	[6]	M	Analysing	CO3
	b)	Truth table [2] Logic diagram [2]	[4]	L	Applying	CO3
	c)	Logic diagram [1] Truth table [1] waveform [2]	[4]	L	Applying	CO3