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*Paper code: P119-142(T1)*

**OCTOBER 2019 / INSEM (T1)**

**F. Y. M. TECH. (E&TC Engineering) (SEMESTER - I)**

**COURSE NAME: ADVANCED EMBEDDED PROCESSORS  
AND PROGRAMMING**

**COURSE CODE: ETPA11182  
(PATTERN 2018:R1)**

Time: [1 Hour]

[Max. Marks: 20]

**(\*) Instructions to candidates:**

- 1) Answer Q.1 OR Q.2, Q.3 OR Q.4
- 2) Figures to the right indicate full marks.
- 3) Use of scientific calculator is allowed
- 4) Use suitable data where ever required

- Q.1) a) With reference to an application, comment on selection of hardware and memory. (6)  
b) Explain suitability of RISC architecture for embedded system. (4)

**OR**

- Q.2) a) What are embedded system design challenges? (6)  
b) How embedded C programming is different than ANSI C programming? (4)
- Q.3) a) Compare coarse and fine granularity in application design with merit and demerits. (6)  
b) What is basis of architecture classification? Illustrate example of SIMD architecture. (4)

**OR**

- Q.4) Illustrate instruction level parallelism and explain any two applications of parallel processing. (10)

Title: Questions Bank - 1/1/19