

Total No. of Questions – [3]

Total No. of Printed Pages: 01

G.R. No.	
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Paper Code- U239-125 (T1)

OCTOBER 2019 / INSEM (T1)
S. Y. B.TECH. (COMPUTER ENGINEERING) (SEMESTER -III)
COURSE NAME: COMPUTER ARCHITECTURE AND ORGANIZATION
COURSE CODE: CSUA21185

MARKING SCHEME
(PATTERN 2018)

Time: [1 Hour]

[Max. Marks: 20]

Instructions to candidates:

- 1) All questions are compulsory.
- 2) Figures to the right indicate full marks.
- 3) Use of scientific calculator is allowed.
- 4) Assume suitable data wherever required.

Q 1) Attempt any **one**

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|----|---------------------------------------|-----------|-----|
| a) | 8086 architecture block diagram. | [4 marks] | [8] |
| | Explanation of BIU and EU. | [4 marks] | |
| b) | 80386 Flag Registers diagram(32 bits) | [4 marks] | [8] |
| | Explanation of each flag. | [4 marks] | |

Q 2) Attempt any **one**

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|----|-------------------------------------|-----------|-----|
| a) | Diagram of Segment Translation. | [4 marks] | [8] |
| | Explanation of Segment Translation. | [4 marks] | |
| b) | Diagram of Paging Mechanism. | [4 marks] | [8] |
| | Explanation of Paging Mechanism. | [4 marks] | |

Q 3) Attempt any **one**

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|----|---|----------------|-----|
| a) | Two Segment Level Protection aspects | [2 marks each] | [4] |
| b) | Description of term CPL, DPL, RPL, and EPL. | [1 mark each] | [4] |