G.R. No.

paper code: U239-125(T1)

OCTOBER 2019 / INSEM (T1)
S. Y. B.TECH. (COMPUTER ENGINEERING) (SEMESTER – III)
COURSE NAME: COMPUTER ARCHITECTURE AND ORGANIZATION
COURSE CODE: CSUA21185

(PATTERN 2018)

Time: [1 Hour]

[Max. Marks: 20]

Instructions to candidates:
1) All questions are compulsory.
2) Figures to the right indicate full marks.
3) Use of scientific calculator is allowed.
4) Assume suitable data where ever required.

Q 1) Attempt any **one**

   a) Draw the 8086 architecture block diagram and explain the functioning of BIU and EU. [8]

   b) Explain 80386 Flag registers. [8]

Q 2) Attempt any **one**

   a) Describe Segment Translation with diagram. [8]

   b) Describe Paging mechanism with diagram. [8]

Q 3) Attempt any **one**

   a) Determine any two Segment Level Protection aspects. [4]

   b) Discuss the term CPL, DPL, RPL, and EPL. [4]