

Total No. of Printed Pages: 01

PAPER CODE	ETUA32181C
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T.Y.B. Tech (E&TC) (SEMESTER - II)
COURSE NAME: Advanced Processors
COURSE CODE: ETUA32181C
(PATTERN 2018)

Time: [1Hr]

[Max. Marks: 30]

- 1) Figures to the right indicate full marks.
- 2) Use of scientific calculator is allowed
- 3) Use suitable data wherever required

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|-----|---|-----|
| Q.1 | a) Any four significant features that justifies its effectivity for an embedded application ----(01x04) | [4] |
| | b) Explanation of Power Management modes
04 modes—1.5 each | [6] |
| | OR | |
| | b) Significance and utility of 04 layers of CMSIS
1.5 mark each | [6] |
| Q2 | a) bit banding method with justification ... method-02 marks
example-02 marks | [4] |
| | b) any 04 differences of power saving modes ...1.5 marks each | [6] |
| | OR | |
| | b) suitability for following category of applications... | |
| | 1. Lighting control ...03 marks | [6] |
| | 2. Motor control ...03 marks | |
| Q.3 | a) need of Using DSP processor in the application---02 marks
Example ---02 marks | [4] |
| | b) significance of MAC in DSP processor... 02 marks
Pipeline details diagram ---02 marks,
explanation ---02 marks | [6] |
| | OR | |
| | b) interface diagram of 07 segment display with LPC1768—02 marks
algorithm /code to display 00 to 99 ---04 marks | [6] |